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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence during pendency of filed application)</i>		Application Number	09/748,098
		Filing Date	December 21, 2000
		First Named Inventor	Nicholas J. Kelsey
		Group Art Unit Number	2183
		Examiner Name	David J. Huisman
Total Number of Pages in This Submission	27	Attorney Docket Number	20880-05093

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IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.  
SERIAL NO.: 09/748,098  
FILING DATE: December 21, 2000  
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching  
EXAMINER: David J. Huisman  
GROUP ART UNIT: 2183  
ATTY. DKT. NO.: 20880-05093

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**AMENDMENT F AND RESPONSE UNDER 37 C.F.R. § 1.116**

Sir:

This amendment for the patent application identified above is in response to the Office Action dated April 13, 2006, which set a shortened statutory period for response that expires on July 16, 2006.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 13 of this paper.

Kindly amend this application as indicated herein.